IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

priority Application Serial No.	10/227.500
<u>pri rity</u> Filing Date	August 22, 2002
Inventor	Wendell P. Noble
Assignee	Micron Technology, Inc.
priority Group Art Unit	2813
priority Examiner	L. Schillinger
Attorney's Docket No	MI22-2378
Title: Semiconductor Processing Methods of Formir	a Integrated Circuitry.
Forming Conductive Lines, Forming a Condu	ctive Grid, Forming a
Conductive Network, Forming an Electrical Inte	erconnection to a Node
Location, Forming an Electrical Interconnect	ion with a Transistor
Source/Drain Region, and Integrated Circuitry	

PRELIMINARY AMENDMENT

To:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

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AMENDMENTS